

MEG03-002

IFW

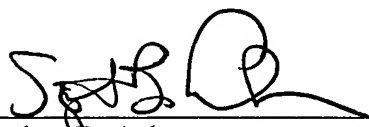
September 29, 2004

TO: Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

FROM: George O. Saile, Reg. No. 19,572
28 Davis Avenue
Poughkeepsie, NY 12603

Subject: Serial #10/802,566
File Date: 3/17/04
Invr(s): Lin, M.S., et al
Title: A High Performance IC Chip Having Discrete Decoupling
Capacitors Attached To Its IC Surface

Enclosed are five sheets of Formal Drawings. Please enter these Drawings to replace the informal Drawings that were submitted with the Patent Application on 3/17/04. The Drawings should be filed under the Serial # 10/802,566. No new matter is added at this time. Thank you.



Stephen B. Ackerman
Reg. No. 37,761

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on September 29, 2004.

Signature 

Stephen B. Ackerman, Reg. No 37,761

Date: 9/29/04